

FIG. 1

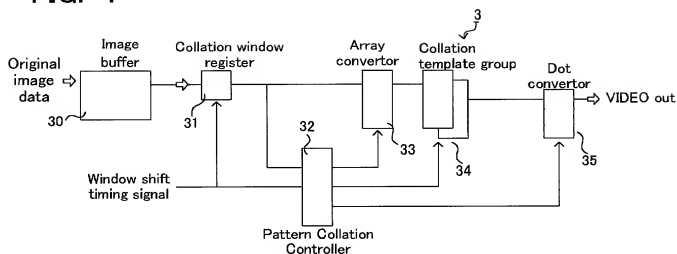


FIG. 2

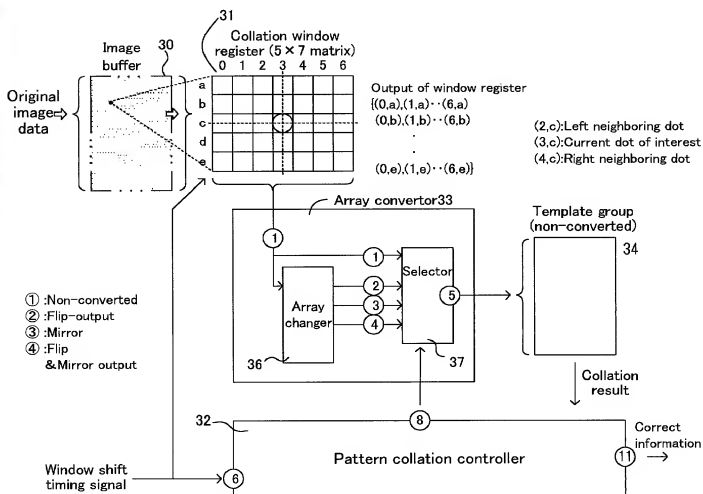


FIG. 4

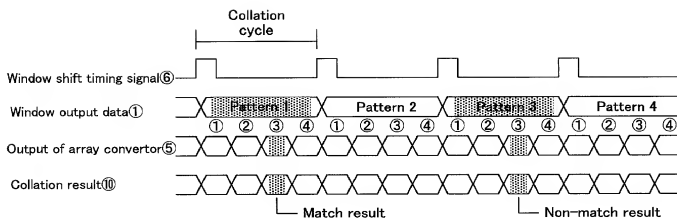


FIG. 5

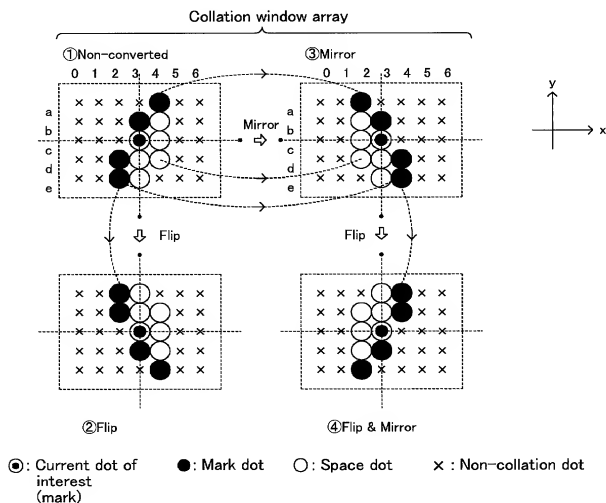


FIG. 6(A)

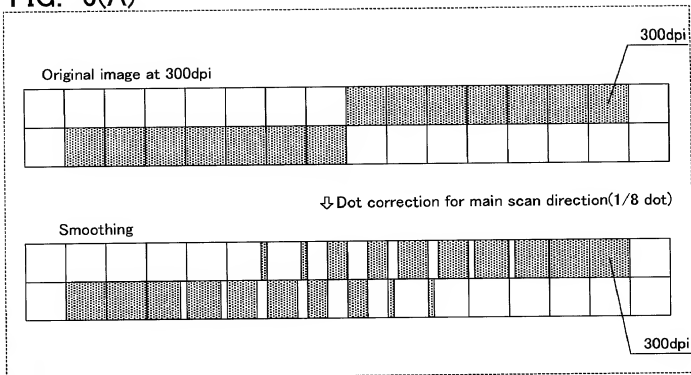


FIG. 6(B)

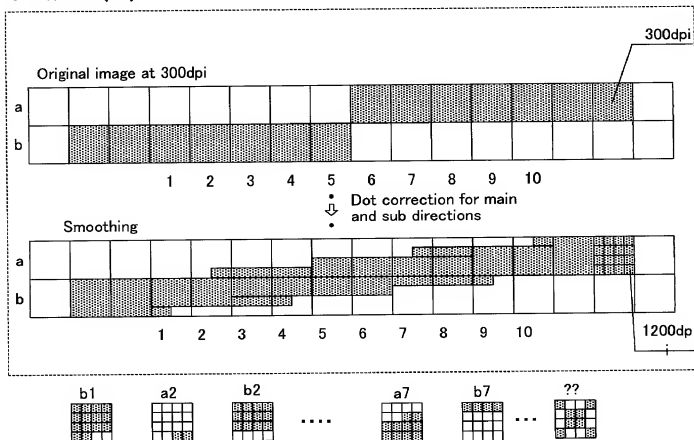


FIG. 7

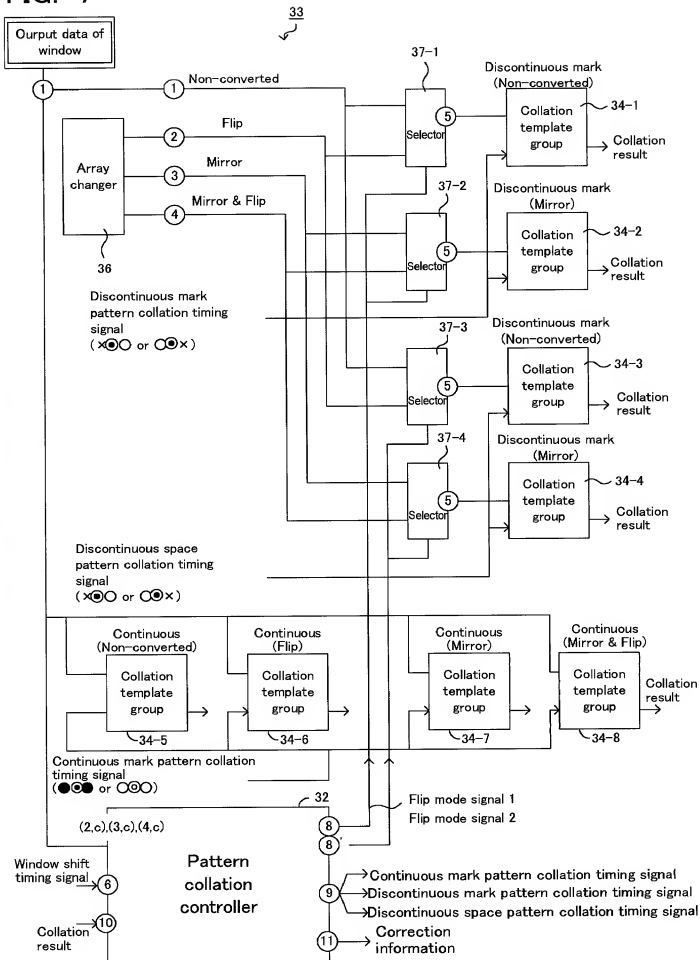


FIG. 8

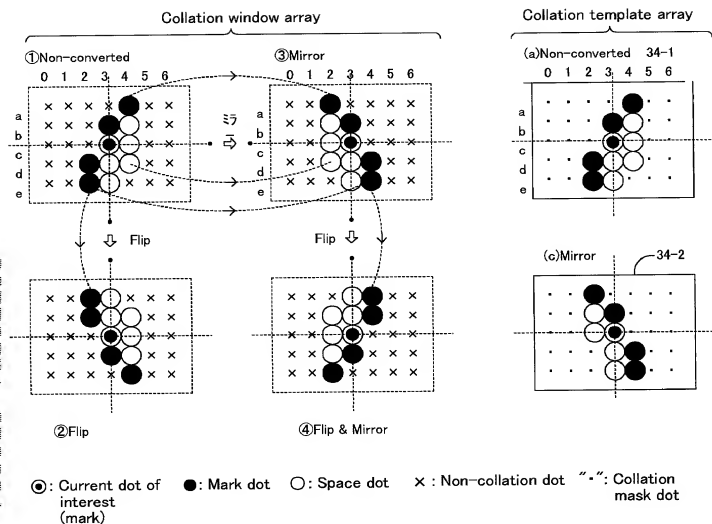


FIG. 9

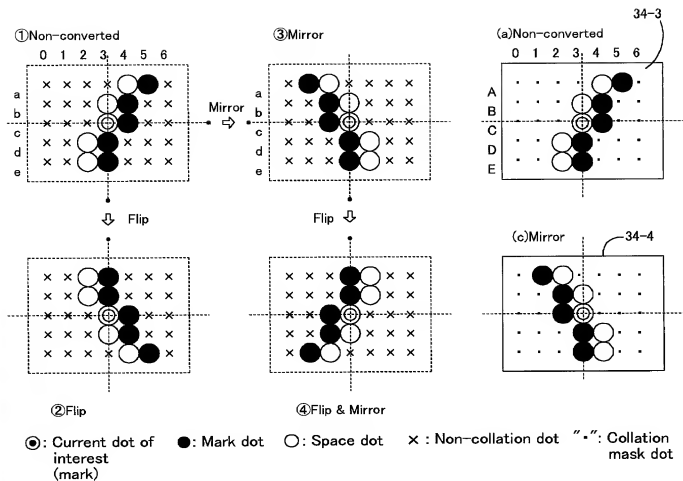


FIG. 10

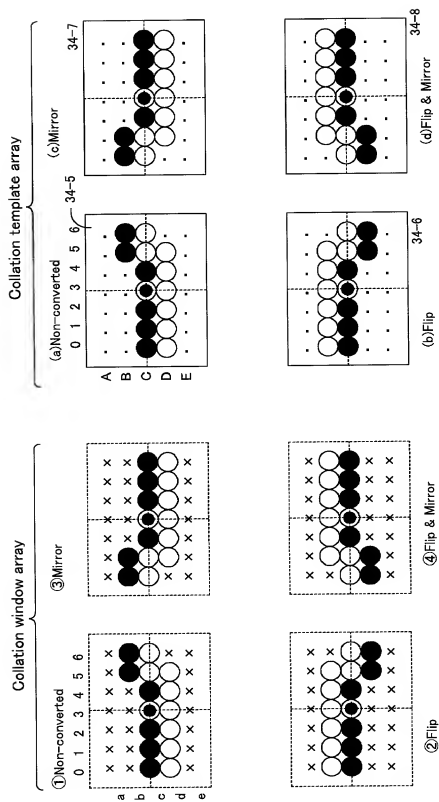


FIG. 11

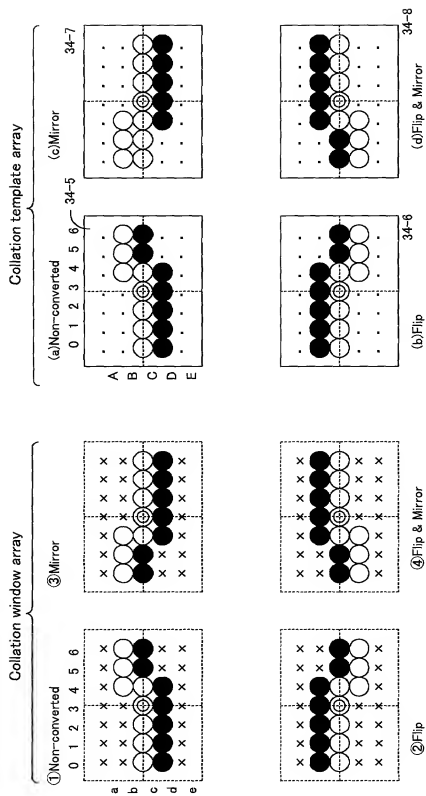


FIG. 12

Window shift timing output ⑥

Window output data ①

Left dot (2,C)

Current dot of interest (3,C)

Right dot (4,C)

Collation timing signal

Continuous pattern

Discontinuous mark pattern

Discontinuous space pattern

Flip mode signal 1 ⑧

Flip mode signal 1 ⑧'

Template collation process

Continuous pattern (Non-converted)

(Mirror)

(Flip)

(Mirror & Flip)

Discontinuous mark pattern (Non-converted)

(Mirror)

Discontinuous mark pattern (Non-converted)

(Mirror)

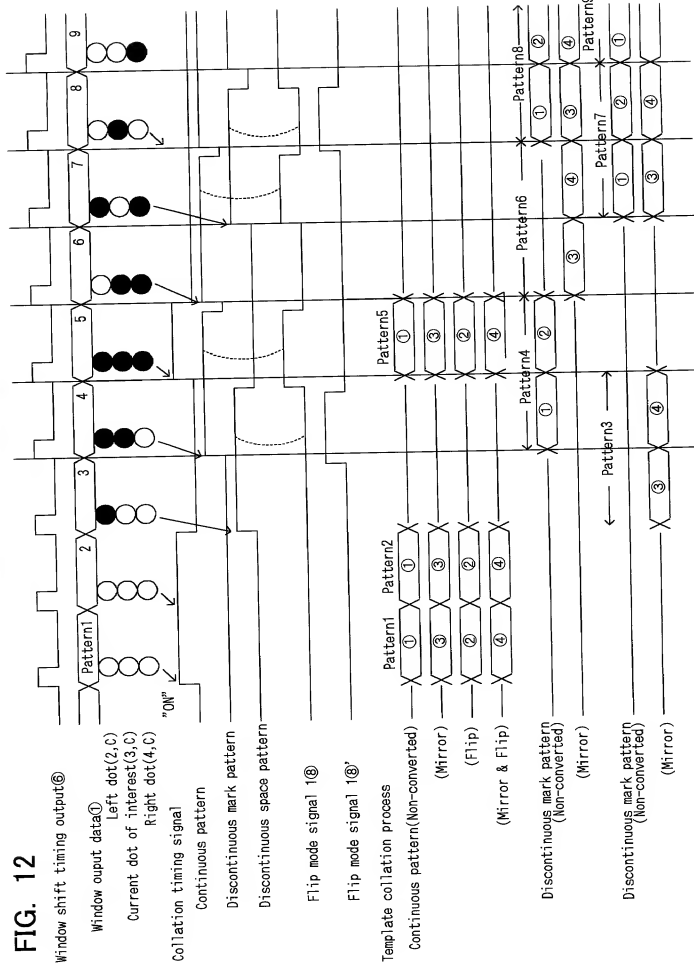
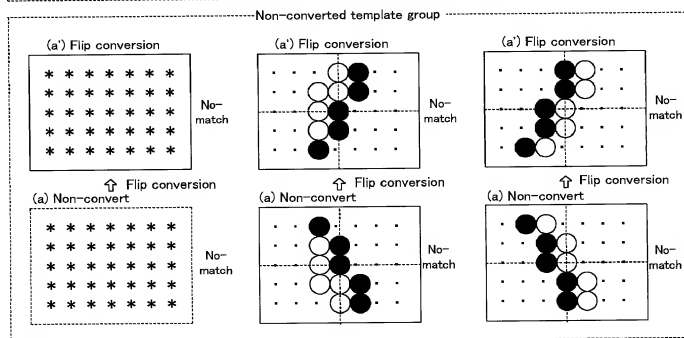
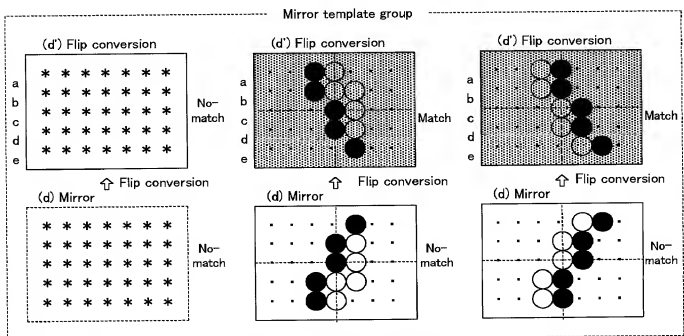
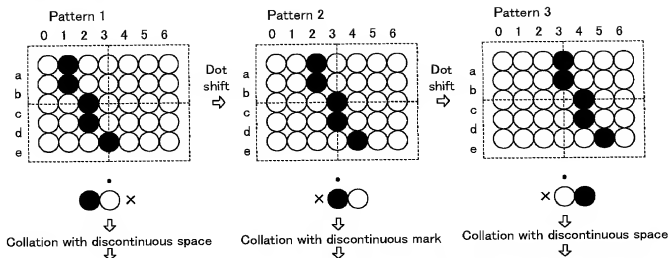


FIG. 13



Questions are asked about the following:



Questions are asked about the following:



FIG. 16

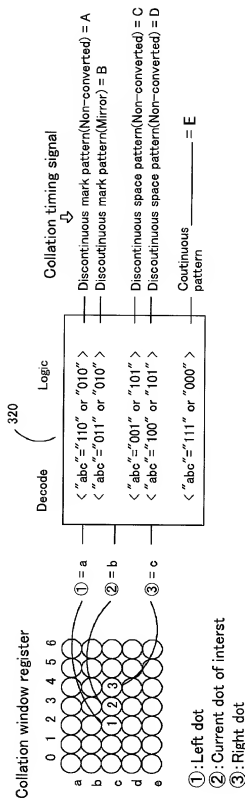


FIG. 17

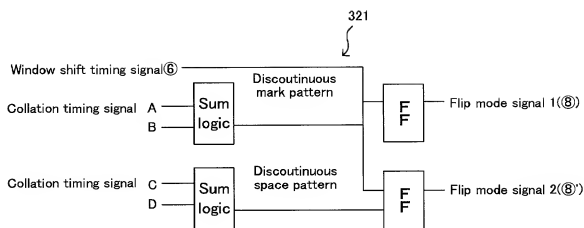


FIG. 18

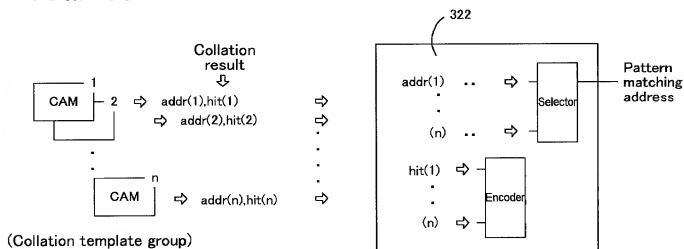


FIG. 19

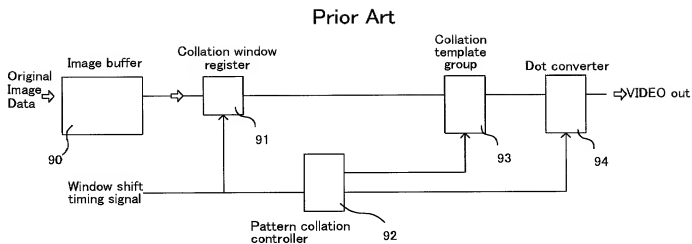


FIG. 20

